

09/989242



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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. REISSUE Patent Application

MT	O.I.P.E.	O.G. PUBLICATION DATE	REISSUE PATENT DATE
SCANNED	HRM ² Q.A.	04/30/02	

APPLICATION NO. 09/989242	CONT/PRIOR D F	CLASS 257	SUBCLASS 666	ART UNIT 1652 3815	EXAMINER J Clark
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APPLICANTS

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Leadframe Semiconductor integrated circuit device using the same and method of and process for fabricating the two

TITLE

5637. 913

PTO-2040
12/89

☐ **SURRENDER OF ORIGINAL PATENT** (Exr. Initials) **ORIGINAL PATENT NUMBER** _____

ISSUING CLASSIFICATION

[illegible]

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